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Relevance scale **181** [TENEX, a paged time sharing system for the PDP - 10](#)

 Daniel G. Bobrow, Jerry D. Burchfiel, Daniel L. Murphy, Raymond S. Tomlinson  
 March 1972 **Communications of the ACM**, Volume 15 Issue 3

**Publisher:** ACM PressFull text available:  [pdf\(932.60 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

TENEX is a new time sharing system implemented on a DEC PDP-10 augmented by special paging hardware developed at BBN. This report specifies a set of goals which are important for any time sharing system. It describes how the TENEX design and implementation achieve these goals. These include specifications for a powerful multiprocess large memory virtual machine, intimate terminal interaction, comprehensive uniform file and I/O capabilities, and clean flexible system structure. Although the ...

**Keywords:** PDP-10, TENEX, paging, process structure, scheduling algorithm, time sharing system, virtual machines

**182** [Bioinformatics \(BIO\): MACE: lossless compression and analysis of microarray images](#)

 Robert Bierman, Nidhi Maniyar, Charles Parsons, Rahul Singh  
 April 2006 **Proceedings of the 2006 ACM symposium on Applied computing SAC '06**

**Publisher:** ACM PressFull text available:  [pdf\(517.72 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The ubiquity of microarray expression data in state-of-the-art biology has been well established. The widespread adoption of this technology coupled with the significant volume of image-based experimental data generated per experiment (averaging 40 MB), have led to significant challenges in storage and query-retrieval of primary data from microarray experiments. Research in the yet nascent area of microarray data-compression seeks to address this problem. In this paper, we propose a conceptually ...

**Keywords:** microarray, microarray data analysis, microarray data compression and storage

**183** [Regular contributions: DRAMsim: a memory system simulator](#)

 David Wang, Brinda Ganesh, Nuengwong Tuaycharoen, Kathleen Baynes, Aamer Jaleel, Bruce Jacob  
 November 2005 **ACM SIGARCH Computer Architecture News**, Volume 33 Issue 4

**Publisher:** ACM PressFull text available:  [pdf\(220.93 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

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